I_{D}

15A



IRF7455PbF

SMPS MOSFET

HEXFET® Power MOSFET

R_{DS(on)} max

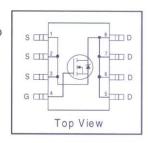
 0.0075Ω

Applications

- High Frequency DC-DC Converters with Synchronous Rectification
- Lead-Free

Benefits

- Ultra-Low R_{DS(on)} at 4.5V V_{GS}
- Low Charge and Low Gate Impedance to Reduce Switching Losses
- Fully Characterized Avalanche Voltage and Current



 V_{DSS}

30V



Absolute Maximum Ratings

Symbol	Parameter	Max.	Units	
V _{DS}	Drain-Source Voltage	30	V	
V _{GS} Gate-to-Source Voltage		± 12	V	
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V	15		
I _D @ T _A = 70°C Continuous Drain Current, V _{GS} @ 10V		12	A	
I _{DM}	Pulsed Drain Current①	120		
P _D @T _A = 25°C Maximum Power Dissipation ³		2.5	W	
P _D @T _A = 70°C	Maximum Power Dissipation3	1.6	W	
	Linear Derating Factor	0.02	W/°C	
T _J , T _{STG}	Junction and Storage Temperature Range	-55 to + 150	°C	

Thermal Resistance

	Parameter	Max.	Units
R _{0JA}	Maximum Junction-to-Ambient⊕	50	°C/W

Typical SMPS Topologies

Telecom 48V Input Converters with Logic-Level Driven Synchronous Rectifiers

Notes ① through ④ are on page 8

Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	30	_	-	V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	_	0.029		V/°C	Reference to 25°C, ID = 1mA
	Static Drain-to-Source On-Resistance		0.0060	0.0075	0	V _{GS} = 10V, I _D = 15A ④
R _{DS(on)}			0.0069 0.009		Ω	V _{GS} = 4.5V, I _D = 12A ④
			0.010	0.020		$V_{GS} = 2.8V, I_D = 3.5A$ ④
$V_{GS(th)}$	Gate Threshold Voltage	0.6	_	2.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
I _{DSS}	Drain-to-Source Leakage Current			20	μА	V _{DS} = 24V, V _{GS} = 0V
			_	100	PΛ	V _{DS} = 24V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Forward Leakage		_	200	^	$V_{GS} = 12V$
	Gate-to-Source Reverse Leakage			-200	nA	V _{GS} = -12V

Dynamic @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
g _{fs}	Forward Transconductance	44	_		S	$V_{DS} = 10V, I_{D} = 15A$
Qg	Total Gate Charge	_	37	56		I _D = 15A
Q _{gs}	Gate-to-Source Charge		8.9	13	nC	$V_{DS} = 24V$
Q _{gd}	Gate-to-Drain ("Miller") Charge	1	13	20	t l	V _{GS} = 5.0V, ③
t _{d(on)}	Turn-On Delay Time		17			V _{DD} = 15V
tr	Rise Time		18	_	ns	$I_D = 1.0A$
t _{d(off)}	Turn-Off Delay Time		51		113	$R_G = 6.0\Omega$
tf	Fall Time	-	44	, ,		V _{GS} = 4.5V ③
Ciss	Input Capacitance		3480	-		V _{GS} = 0V
Coss	Output Capacitance	_	870	_		$V_{DS} = 25V$
Crss	Reverse Transfer Capacitance	10	100		pF	f = 1.0MHz

Avalanche Characteristics

	Parameter	Typ.	Max.	Units
E _{AS}	Single Pulse Avalanche Energy®		200	mJ
I _{AR}	Avalanche Current®		15	A
E _{AR}	Repetitive Avalanche Energy®	_	0.25	mJ

Diode Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current (Body Diode)	-	_	2.5	^	MOSFET symbol showing the
I _{SM}	Pulsed Source Current (Body Diode) ①	_	_	120	A	integral reverse p-n junction diode.
V _{SD}	Diode Forward Voltage		_	1.2	٧	$T_J = 25^{\circ}C$, $I_S = 2.5A$, $V_{GS} = 0V$ ③
t _{rr}	Reverse Recovery Time	_	64	96	ns	T _J = 25°C, I _F = 2.5A
Qrr	Reverse RecoveryCharge	-	99	150	nC	di/dt = 100A/µs ③

IRF7455PbF

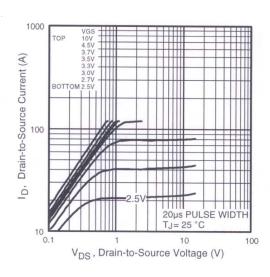


Fig 1. Typical Output Characteristics

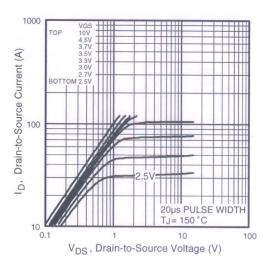


Fig 2. Typical Output Characteristics

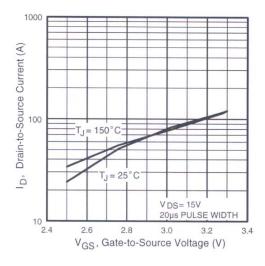


Fig 3. Typical Transfer Characteristics

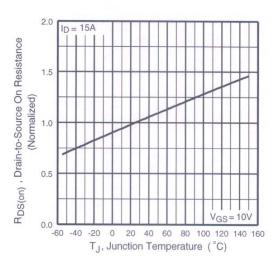


Fig 4. Normalized On-Resistance Vs. Temperature

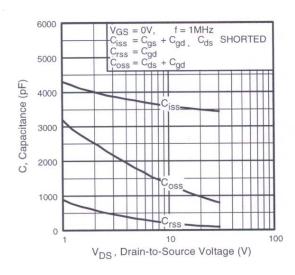


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

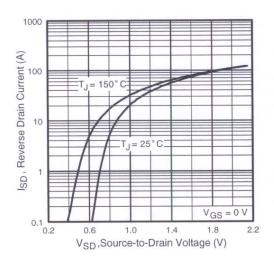


Fig 7. Typical Source-Drain Diode Forward Voltage

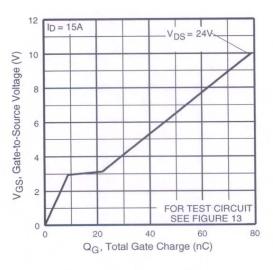


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

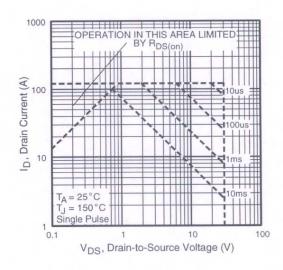


Fig 8. Maximum Safe Operating Area

IRF7455PbF

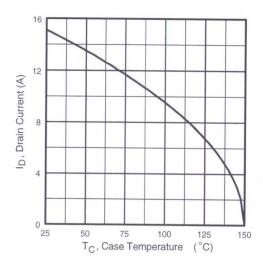


Fig 9. Maximum Drain Current Vs. Case Temperature

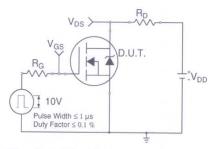


Fig 10a. Switching Time Test Circuit

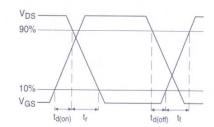


Fig 10b. Switching Time Waveforms

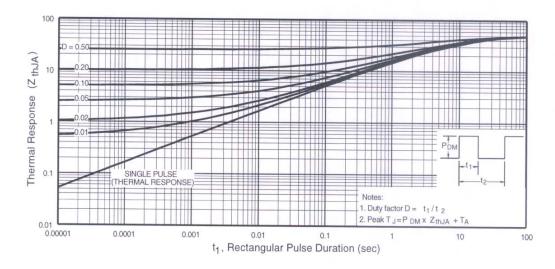


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

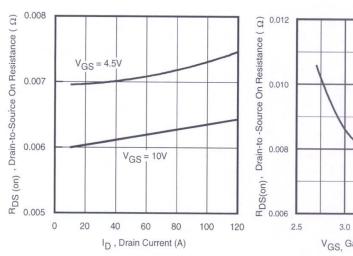


Fig 12. On-Resistance Vs. Drain Current

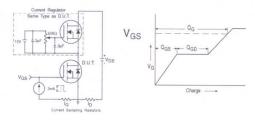


Fig 13a&b. Basic Gate Charge Test Circuit and Waveform

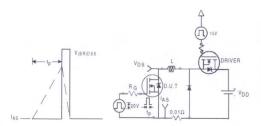


Fig 14a&b. Unclamped Inductive Test circuit and Waveforms

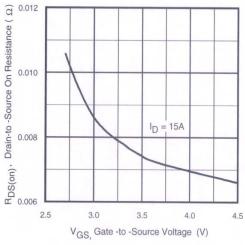


Fig 13. On-Resistance Vs. Gate Voltage

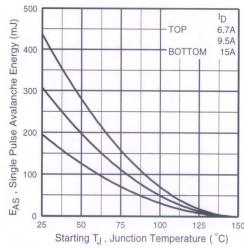
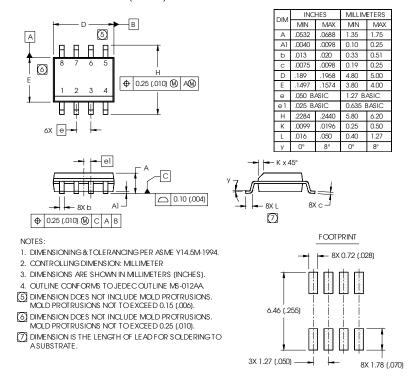


Fig 14c. Maximum Avalanche Energy Vs. Drain Current

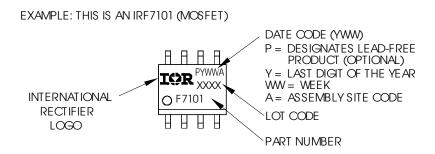
IRF7455PbF

SO-8 Package Outline

Dimensions are shown in milimeters (inches)

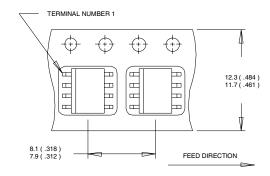


SO-8 Part Marking Information (Lead-Free)

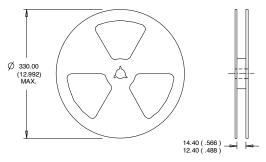


SO-8 Tape and Reel

Dimensions are shown in milimeters (inches)



- CONTROLLING DIMENSION: MILLIMETER.
 ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
 OUTLINE CONFORMS TO EIA-481 & EIA-541.



- CONTROLLING DIMENSION : MILLIMETER.
 OUTLINE CONFORMS TO EIA-481 & EIA-541.

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ③ Pulse width \leq 300 μ s; duty cycle \leq 2%.
- ② Starting $T_J = 25^{\circ}C$, L = 1.8mH $R_G = 25\Omega$, $I_{AS} = 15A$.
- When mounted on 1 inch square copper board, t<10 sec</p>

Data and specifications subject to change without notice. This product has been designed and qualified for the Consumer market. Qualifications Standards can be found on IR's Web site.

> International IOR Rectifier

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105 TAC Fax: (310) 252-7903

Visit us at www.irf.com for sales contact information.06/04

IMPORTANT NOTICE

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie").

With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office (www.infineon.com).

WARNINGS

Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.